

9. Circuit according to Claim 8, wherein in order to determine the first or second state, the first device has units for producing controlled characteristics for assessment of field differences as a function of line differences, with the motion sensitivity being increased if the line difference are small, and the motion sensitivity being reduced if the line differences are large.

10. Circuit according to Claim 9 wherein the first device has circuit units for forming line and field differences, with the field difference being assessed by the units for producing line differences are applied and being mapped onto 1-bit signals, and these 1-bit signals being logically combined by an OR gate in order to produce the pixel motion signals.

11. Circuit according to claim 10 wherein the third, fourth and the fifth circuit unit are used to produce three field differences from a first, a second and a third field, and in that the units for producing controlled characteristics are controlled using the maximum of the line difference signals from the first and second field.

12. Circuit according to 8 wherein the second device comprises a correction unit for processing the motion signals of each pixel in such a manner that the first state is corrected to the second state if the motion signals of all the adjacent pixels are in the second state, with a previously corrected state being used for the processing of a subsequent pixel.

13. Circuit according to claim 8 wherein the second device comprises a correction unit for processing all the motion signals in a line in such a manner that the first state is changed to the second state if the motion signals in one and two lines above and in one line underneath are in the second state.

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